

In the Specification

Please replace paragraph [0004] with the following annotated paragraph:

[0004] FIG. 1 is a schematic illustration showing a write delay time of a memory. After the write command W is issued, the memory has a minimum write delay time t_{DQSSm} . That is, after at least the write delay time t_{DQSSm} has elapsed, a data strobe signal DQS may be switched to a high level so that data may be written to the memory. The memory additionally has a maximum ~~buffer write delay~~ time t_{DQSSM} . That is, before the time elapses, the data strobe signal DQS has to be switched to a high level in order to write the data to the memory. Thus, the data written to the memory may be ensured to be correct as long as the write delay time falls between the minimum ~~buffer write delay~~ time t_{DQSSm} and the maximum ~~buffer write delay~~ time t_{DQSSM} .